



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Donald V. Smart
Serial No. : 10/036,431
Filed : January 7, 2002
Title : LASER PROCESSING

Art Unit : 1725
Examiner : Samuel M. Heinrich

Commissioner for Patents
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Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449.

Submitted herewith is an English translation of the following foreign language references, or portions thereof: JP62086851A2 (English-language abstract and drawing only).

Under 35 USC §120, this application relies on the earlier filing date of application serial number 08/774,107, filed on December 24, 1996 and application serial number 09/441,201, filed on November 16, 1999. All of the references, with the exception of the following, were submitted to and/or cited by the Office in at least one of the prior applications and, therefore, are not provided in this application:

Konecny, U.S. Patent No. 5,453,594;

Smart, U.S. Patent No. 5,998,759;

Smart, U.S. Patent No. 6,337,462;

JP62086851A2 (English-language abstract and drawing only)

General Scanning Inc., "Laser Link Fusing: Guidelines for the Applications of M3XX Laser Systems in Semiconductor Link Fusing Processes," January, 1997;

General Scanning Inc., "Mx Laser Processing Systems Applications Manual," pages 3-1 through 3-6, 1994;

Teradyne, Inc. "Enhancements for Productivity and Simplified Maintenance: Link Sprint™, Sun 3 Workstation, Diode-Pumped Nd:YLF Laser: Three New Options for the M218 Laser Processing System," 1988.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

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Page : 2 of 2

Attorney's Docket No.: 06457-007003

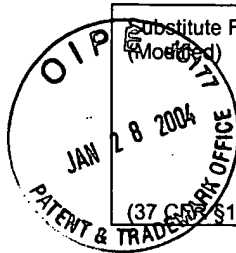
Respectfully submitted,

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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 06457-007003	Application No. 10/036,431
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR § 1.98(b))		Applicant Donald V. Smart	
		Filing Date January 7, 2002	Group Art Unit 1725

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,208,437	05/1993	MIYAUCHI et al.			
	AB	5,021,362	06/1991	CHLIPALA			
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	AR	5,696,778	12/1997	MacPherson			
	AS	5,998,759	12/1999	Smart			
	AT	6,337,462	01/2002	Smart			
	AU	Re 34,192	03/1993	Bear			
	AV	5,453,594	09/26/1995	KONECNY			

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
	AW	5-104266	04/1993	JAPAN			Yes	No
	AX	WO 94/06182	03/1994	PCT				

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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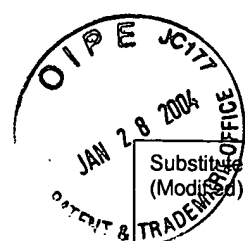
Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AY	WO 96/12830	05/1996	PCT *				
	BA	WO 96/15870	05/1996	PCT *				
	BB	JP62086851A2	4/1987	Japan	H01L 27	01	X (English-language abstract only)	

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	BB	Cook, P.W. et al.; "Connections and Disconnections on Integrated Circuits Using Nanosecond Laser Pulses"; Appl. Phys. Lett., Vol. 26, Number 3; pp. 124-126; February 1, 1975
	BC	Faber, Jr, Marshall; "Algorithmic Trimming on Active Circuitry"; Proceedings of the Electronic Components Conference, Washington DC; pp. 248-254, 1974
	BD	General Scanning Inc., Laser Systems Division; "M325 Memory Repair System" brochure; 32 Cobble Hill Road, Somerville, Massachusetts
	BE	Gillespie, D.T. et al.; "Transmittance of Optical Materials at High Temperature in the 1-μ to 12-μ Range"; Applied Optics; Vol. 4, No. 11; pp. 1488-1493; 1965
	BF	Hutchens; "New Laser Technology Addresses the Yield Enhancement Challenge of the Evolving Generation of Memory Devices"; Electro Scientific Industries, Inc.; Portland OR 97229 USA; 1993
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	BH	Kestenbaum et al.; "Photoexcitation Effects During Laser Trimming of Thin-Film Resistors on Silicon"; IEEE Transactions on Components, Hybrids, and Manufacturing Technology; Vol. CHMT-3, No. 1; 1980
	BI	Kestenbaum, Ami; "Semiconductor Processing Applications With Lasers"; Proceedings of the Technical Program, Int'l. Microelectronics Conference, Anaheim CA; pp 235-244; Feb. 23, 1982
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	BK	Litwin et al.; "Laser Adjustment of Linear Monolithic Circuits"; Technical Digest of Int'l. Congress on Applications of Lasers and Electro-Optics, L.I.A; p. 32; November 14-17, 1983
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	BM	Murray et al.; "Generation of 1.5-μm Radiation Through Interactivity Solid-State Raman Shifting in Ba(NO ₃) ₂ Nonlinear Crystals"; Optics Letters; vol. 20, No. 9; pp. 1017-1019; 1995
	BN	James C. North et al.; "Laser Coding of Bipolar Read-Only Memories"; IEEE Journal of Solid-State Circuits; pp. 500-505; August 1976.
	BO	James C. North et al.; "Laser Vaporization of Metal Films - Effect of Optical Interference in Underlying Dielectric Layers"; Journal of Applied Physics; vol. 48, No. 5; pp 2419-2423; June 1977
	BP	Price et al.; "A passive Laser-Trimming Technique to Improve the Linearity of a 10-Bit D/A Converter"; IEEE Journal of Solid-State Circuits; Vol. SC-11, No. 6; pp. 784-794; December 1976
	BQ	Runyan, W.R.; Silicon Semiconductor Technology; Texas Instruments Electronics Series; Chapter 9; page 187; McGraw Hill, New York; 1965.

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	BR	Scarfone, et al.; "Computer Simulation of Target Link Explosion in Laser Programmable Redundancy for Silicon Memory; J Mater. Res.; vol. 1, No. 2; pp. 367-381; March/April, 1986.
	CA	Shui et al.; "Effect of Vapor Plasma on the Coupling of Laser Radiation with Aluminum Targets"; J. Quant. Spectrosc. Radiat. Transfer; vol. 20, pp. 627-636; 1978.
	CB	Donald V. Smart; "Considerations of Laser Material Interactions for the Trimming of Thin Film Resistors"; IEEE Int'l. Symp. on Circuits and Systems Proceedings, Montreal, Canada; May 7, 1984
	CC	Donald V. Smart; "Implementing Memory Redundancy: Laser Beam Alignment Targets"; Teradyne Application Report 149B; pp. 1-8.
	CD	Donald V. Smart et al.; "Laser Processing for Application Specific Integrated Circuits (ASICs); SPIE vol. 774 Lasers in Microlithography; pp. 88-92; 1987.
	CE	Donald V. Smart; "Optimization of Semiconductor Layer Thicknesses for Repair of RAMs"; Teradyne Application Report 150; pages 1-11.
	CF	Donald V. Smart; "System Considerations for the Laser Repair of Semiconductor Memories"; Technical Digest of the Conference on Lasers and Electro-Optics; Balt., MD; May 17-20, 1983.
	CG	Harris Sun et al.; "Optimization of Memory Redundancy Laser Link Processing"; SPIE; vol. 2636; June 1995.
	CI	Wells et al.; "A Technique for Automatic Focusing and Alignment of Monolithic Circuits for Laser Trimming"; Proceedings of the Third Annual Microelectronic Measurement Technology Seminar; March 1981.
	CJ	General Scanning Inc., "Laser Link Fusing: Guidelines for the Applications of M3XX Laser Systems in Semiconductor Link Fusing Processes," January, 1997
	CK	General Scanning Inc., "Mx Laser Processing Systems Applications Manual," pages 3-1 through 3-6, 1994
	CL	Teradyne, Inc. "Enhancements for Productivity and Simplified Maintenance: Link Sprint™, Sun 3 Workstation, Diode-Pumped Nd:YLF Laser: Three New Options for the M218 Laser Processing System," 1988
	CM	Hecht, "The Laser Guidebook, 2nd Edition," New York, McGraw Hill, 1992, pp. 49, 50, 60-67, 433
	CN	Brannon, James H.; "Excimer-Laser Ablation and Etching"; Circuits and Devices, September 1990

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